

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,443,240 B2  
APPLICATION NO. : 10/580167  
DATED : October 28, 2008  
INVENTOR(S) : Hiroshi Katsunaga et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On Title Page 1, Item (73), please delete “Kabushiki Kaisha Toyota Jidoshokki, Kariya-Shi (JP); Niigata Seimitsu Co., Ltd., Joetsu-Shi (JP)” and insert therefore -- Niigata Seimitsu Co., Ltd., Joetsu-Shi (JP) --;

Column 1, line 45, please delete “referring to FIG. 5(a)-5(c).” and insert therefore -- referring to FIGS. 5(a)-5(c). --;

Column 1, lines 63-64, please delete “resulting in a reduced the drain current Id4” and insert therefore -- resulting in a reduced drain current Id4 --;

Column 2, lines 11-12, please delete “if the input signal level increase” and insert therefore -- if the input signal level increases --;

Column 2, lines 14-15, please delete “When it is considered that the current source 11 of the variable gain amplifier circuit 10” and insert therefore -- The current source 11 of the variable gain amplifier circuit 10 --;

Column 2, lines 40-41, please delete “if a control voltage applied to the gate of a p-channel MOS transistor 25,” and insert therefore -- if a control voltage is applied to the gate of a p-channel MOS transistor 25, --;

Column 3, lines 14-15, please delete “for controlling the differential amplification gain of the first and second field-effect transistors, and a bias” and insert therefore -- for controlling the differential amplification gain of the first and second field-effect transistors is applied, and a bias --;

Column 4, line 51, please delete “variable gain amplifier circuit;” and insert therefore -- variable gain amplifier circuit; and --;

Column 5, line 63, please delete “Since constant current Is is supplied” and insert therefore -- Since constant current Is is supplied --;

Column 6, line 62, please delete “a current source 44 form a current mirror circuit,” and insert therefore -- a current source 44 forming a current mirror circuit, --;

Column 8, lines 27-28, please delete “and the resistor R8, it can also comprise one MOS transistor.” and insert therefore -- and the resistor R8, but can also comprise one MOS transistor. --; and

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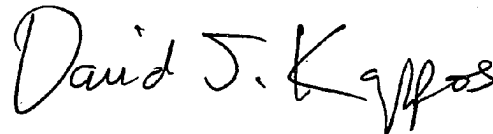
Column 8, lines 33-35, please delete “radio set and is can also be applied to the variable gain amplifier circuit of various circuits.” and insert therefore -- radio set, but can also be applied to the variable gain amplifier circuit of various circuits. --.

In Claim 3, column 9, lines 9-10, please delete “between a source of the first field-effect transistors” and insert therefore -- between a source of the first field-effect transistor --; and

In Claim 6, column 10, lines 1-3, please delete “a fifth field-effect transistor comprising source connected to a power supply” and insert therefore -- a fifth field-effect transistor comprising a source connected to a power supply --.

Signed and Sealed this

Twenty-fifth Day of August, 2009

A handwritten signature in black ink that reads "David J. Kappos". The signature is written in a cursive, flowing style with a large initial 'D' and a stylized 'K'.

David J. Kappos  
*Director of the United States Patent and Trademark Office*